

UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S) IHS, HASSAN GROUP ART UNIT: 2816
APPLN. NO.: 10/596043 EXAMINER: Hiep Nguyen
FILED: 12/21/2006
TITLE: CLOCK PULSE GENERATOR APPARATUS WITH REDUCED
JITTER CLOCK PHASE

Certificate of Submission

I hereby certify that this correspondence is being submitted to the U.S.P.T.O., Alexandria, VA.

- ☐ Addressed per C.F.R. § 1.1(a) and deposited with the United States Postal Service with sufficient postage as first class mail.
- ☐ Facsimile transmitted in accordance with C.F.R. § 1.6(d).
- ☒ Submitted electronically via EFS in accordance with "Legal Framework for EFS Web".

October 16, 2009

Date of Submission

/Dee Matocha/

Signature

Dee Matocha

Printed Name of Person Signing Certificate

RESPONSE TO OFFICE ACTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action dated June 18th 2009, and Examiner's comments with regard thereto, please enter the following amendments and remarks in the above-entitled application, without prejudice or disclaimer.

DESCRIPTION

Amend page 8 lines 20 to 22 as follows:

“In a typical application to a sigma delta ~~module~~modulator, the active phase ACP is preferably used as the phase where the integration is performed while the NACP is used for returning to zero.